IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Ritsuko Iwasaki

Serial No.: 09/496,421

Filing Date: February 2, 2000

Group Art Unit: 2815

Examiner: Lee, E.

SEMICONDUCTOR DEVICE HAVING AN IMPROVED LAYOUT PATTERN

OF PAIR TRANSISTORS

Honorable Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

able Assistant Commissioner of Patents
ngton, D.C. 20231

NOTICE OF CHANGE OF ADDRESS

Please be advised that the correspondence address of attorneys of record in the aboveied application has been aboved to identified application has been changed to:

> McGINN & GIBB, PLLC 8321 OLD COURTHOUSE RD. **SUITE 200** VIENNA, VA 22182-3817 (703) 761-4100

Customer No.: 21254

Please change the records in regard to the above-identified application accordingly and direct all telephone calls to the number shown above.

Respectfully submitted,

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